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scratch-pad size

Nghi Nguyen, Angel Dominguez, Rajeev Barua

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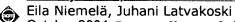
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This paper presents the first memory allocation scheme for embedded systems having scratch-pad memory whose size is unknown at compile time. A scratch-pad memory (SPM) is a fast compiler-managed SRAM that replaces the hardware-managed cache. Its uses are motivated by its better real-time quarantees as compared to cache and by its significantly lower overheads in energy consumption, area and access time. Existing data allocation schemes for SPM all require that the SPM size be known at compile-tim ...

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Ubiquitous computing embeds computer technology in our everyday environment, providing a human with information services and applications through any device over different kinds of networks. Ubiquitous computing can be seen as a prerequisite for pervasive computing that emphasizes mobile data access and the mechanisms needed for supporting a community of nomadic users. Ubiquitous software is the software required in ubiquitous computing environments. This paper surveys the challenges and state-o ...